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APPLICANTS *O.K. TYT*

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** CONTINUING DATA *****
 This application is a DIV of 10/267,365 10/09/2002 PAT 6,713,325 *O.K. TYT*

** FOREIGN APPLICATIONS *****
 JAPAN 2002-000702 01/07/2002 *O.K. TYT*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>TYT</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 4
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TITLE
 Method of manufacturing a semiconductor integrated circuit and semiconductor integrated circuit

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